- 1 6. (Unchanged) A method for dithering pixel color in a graphics system that displays a
- 2 group of pixels in which primary pixel colors are represented by color shades having fewer than
- 3 eight bits comprising the steps of:
- generating an eight bit color shade value for each pixel representing a desired color for each pixel;
- 6 (b) truncating the desired eight bit color shade value to produce a first color shade value comprising fewer than eight bits;
- generating a FRAC value for each pixel representing the truncated bits of said desired eight bit color shade value;

(d) producing a ramp value for each pixel using said FRAC value, wherein said ramp value encodes a discrepancy between the desired eight bit color shade value and the first color shade value;

- (e) producing an addend value for incrementing said first color shade value;
- incrementing said first color shade value by said addend value to produce a second color shade value;
- 16 (g) mapping a dither value to a bit position within said ramp value; and
 17 (h) selecting said first color shade value or said second color shade value to
 18 determine the color of each pixel in said group of pixels.
- 1 12. (Amended) A graphics system that displays color shades based upon binary
- 2 representation having fewer than eight bits, wherein said graphics system initially receives a
- 3 desired eight bit binary representation for each color shade that is used by the graphics system to
- 4 render pixels in a pixel grid, said desired eight bit binary representation including upper order
- 5 bits and lower order bits, comprising:

select fractional logic that receives the desired eight bit binary representation and wherein said select fractional logic produces on its output lines the lower order bits of said desired eight bit binary representation value;

a look-up table that produces a control value based upon an address of each pixel; and ramp probability logic coupled to said select fractional logic [and said look-up table], said ramp probability logic producing a ramp value that encodes a discrepancy between said desired eight bit binary representation and said binary

representations having fewer than eight bits[.] and

mapping logic coupled to said look-up table and ramp probability logic, said mapping.

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